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Technology Center 2100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Group Art Unit: 2122

Kenneth J. Hines

Application No. 09/888,061

Filed: June 21, 2001

For: **EVOLUTION DIAGRAMS FOR DEBUGGING DISTRIBUTED EMBEDDED SOFTWARE APPLICATIONS**

Date: December 13, 2002

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO:
COMMISSIONER FOR PATENTS
WASHINGTON, DC 20231
ON: December 13, 2002
Date
Signature
Connie English
Typed Name

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TRANSMITTAL LETTER

TO THE COMMISSIONER FOR PATENTS:

Enclosed for filing in the above-referenced application are the following:

[✓] Supplemental Information Disclosure Statement
[✓] Form PTO-1499, together with copies of cited references
[✓] Return receipt postcard

The Commissioner is hereby authorized to charge any additional fees which may be required in connection with filing of these papers, or credit overpayment, to Account No. 19-4455. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

Kenneth J. Hines

By _____
Steven J. Munson
Registration No. 47,812

STOEL RIVES LLP
900 SW Fifth Avenue, Suite 2600
Portland, Oregon 97204-1268
Telephone: (503) 224-3380
Facsimile: (503) 220-2480
Attorney Docket No. 10488/10:1

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re application of

Kenneth J. Hines

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Filed: June 21, 2001

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Date: December 13, 2002

Examiner:

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WASHINGTON, DC 20231

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Signature

Connie English
Typed Name

Ace 1/24/03 #11

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

TO THE COMMISSIONER FOR PATENTS:

1. Pursuant to his duty of disclosure, applicant presents for the Examiner's consideration the documents listed on the accompanying Form PTO-1449.

[✓] Copies of the documents are enclosed.

[ ] Copies (a copy) of document(s) _____ are (is) not enclosed because (1) they are already of record in U.S. Pat. Application No. _____, filed _____, on which this application relies for an earlier filing date under 35 U.S.C. § 120, and (2) any information disclosure statement filed in the prosecution of U.S. Pat. Application No. _____, complies with 37 C.F.R. §§ 1.98(a) and (c). (37 C.F.R. § 1.98(d).)

2. [ ] Applicant(s) draw(s) the Examiner's attention to the enclosed copy of copending U.S. Pat. Application No. _____, filed _____, for _____, which is cited in this application.

3. This information disclosure statement is being submitted:

a. [✓] Within three months of the filing date of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office action on the merits; or before the mailing of a first Office action after the filing of a request for continued examination under 37 CFR 1.114. (No statement under 37 CFR 1.97(e) is required.)

b. [ ] After the period set forth in paragraph 1a, but before the mailing date of either a final action, a notice of allowance, or an action that otherwise closes prosecution in the application. (Check box i. or ii.)

i. [ ] A $180.00 information disclosure statement submission fee set forth in 37 CFR 1.17(p) is enclosed.

ii. [ ] A statement specified by 37 CFR 1.97(e) is set forth below.

c. [ ] After the mailing date of a final action or notice of allowance and on or before payment of an issue fee. A statement specified by 37 CFR 1.97(e) is set forth below. Enclosed is a $180.00 information disclosure statement processing fee set forth in 37 CFR 1.17(p).

4. [ ] The attorney or agent signing below hereby states that:

[ ] each item of information contained in the information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement; or

[ ] no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement.

5. [ ] Applicants set forth below concise explanations of the relevance of each document not in the English language and/or selected documents in the English language.

Respectfully submitted,

Kenneth J. Hines

By _____
Steven J. Munson
Registration No. 47,812

STOEL RIVES LLP
900 SW Fifth Avenue, Suite 2600
Portland, Oregon 97204-1268
Telephone: (503) 224-3380
Facsimile: (503) 220-2480
Attorney Docket No. 10488/10:1

Paper # 11

FORM PTO-1449 (REV. 7-80)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE CITATION
(Uses several sheets if necessary)

OIPE JC68 DEC 16 2002 PATENT & TRADEMARK OFFICE

ATTY. DOCKET NO.	APPLICATION NO.
10488/10:1	09/888,061
APPLICANT	
Kenneth J. Hines	
FILING DATE	GROUP
June 21, 2001	2122

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5 4 2 8 7 8 2	6/27/95	White	395	650	
	AB	5 6 4 2 4 7 8	6/24/97	Chen, et al.	395	183.21	
	AC	5 7 2 4 5 0 8	3/3/98	Harple, Jr. et al.	395	200.04	
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO
	AL	EP 0 881 568 A2	29.05.1998	Europe	G06F	9/46		
	AM	EP 0 822 484 A2	04.02.1998	Europe	G06F	9/44		
	AN							
	AO							
	AP							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	Ken Hines and Gaetano Borriello, "A Geographically Distributed Framework for Embedded System Design and Validation," *DAC 98, June 15-19, 1998, San Francisco, CA USA*, pp. 140-145.
	AS	Gopalan Suresh Raj, "A Detailed Comparison of CORBA, DCOM, and Java/RMI," *Web Cornucopia*, wysiwyg://402/http://gsraj.tripod.com/misc/compare
	AT	"CORBA Overview," *CORBA v2.0*, July 1995, pp. 2-1 to 18-5

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformation with MPEP609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449 (REV. 7-80)

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	AR	Richard J. Anderson, Paul Beame, Steve Burns, et al., "Model Checking Large Software Specifications," *Sigsoft '96 California, USA*, pp. 156-166.
	AS	Christopher B. Borchert, "Organization and Management of Distributed Execution Event Histories For Real-Time Debugging," *IEEE*, pp. 343-345.
	AT	Mariano P. Consens, Masum Z. Hasan, Alberto O. Mendelzon, "Visualizing and Querying Distributed Event Traces with Hy+," *Computer Systems Research Institute, University of Toronto*, June 1994, pp. 123-141.

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	AR	Wentong Cai, Wendy J. Milne and Stephen J. Turner, "Graphical Views of the Behavior of Parallel Programs," *Journal of Parallel and Distributed Computing*, June 18, 1993, No. 2, Orlando, FL, USA, pp. 223-230.
	AS	Matthew S. Jaffe, Nancy G. Leveson, Mats P.E. Heimdahl, et al., "Software Requirements Analysis for Real-Time Process-Control Systems," *8198 IEEE Transactions on Software Engineering*, March 17, 1991, No. 3, New York, US, pp. 241-257.
	AT	Thomas Kunz, David J. Taylor, and James P. Black, "Poet: Target-System-Independent Visualizations of Complex Distributed Executions, *IEEE*, 1997, pp. 452-461.

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	AR	Doug Kimelman and Dror Zernik, "On-the-Fly Topological Sort – A Basis for Interactive Debugging and Live Visualization of Parallel Programs," *8398 ACM Sigplan Notices*, December 28, 1993, No. 12, New York, US, pp. 12-20.
	AS	Mikhail Auguston, "Building Program Behavior Models," *Dept. of Computer Science, New Mexico State University*, pp. 19-26.
	AT	Stephen Edwards, Luciano Lavagno, Edward A. Lee, et al., "Design of Embedded Systems: Formal Models, Validation, and Synthesis," *Proceedings of the IEEE*, Vol. 85, No. 3, March 1997, pp. 366-389.

EXAMINER | DATE CONSIDERED

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	AR	Pai Chou, Ross Ortega, Ken Hines, et al., "IPChinook: An Integrated IP-based Design Framework for Distributed Embedded Systems," pp. 44-49.
	AS	Pai Chou, Ken Hines, Kurt Partridge and Gaetano Borriello, "Control Generation for Embedded Systems Based on Composition of Modal Processes," pp. 46-53.
	AT	Pai H. Chou and Gaetano Borriello, "Synthesis and Optimization of Coordination Controllers for Distributed Embedded Systems," pp. 410-415.

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